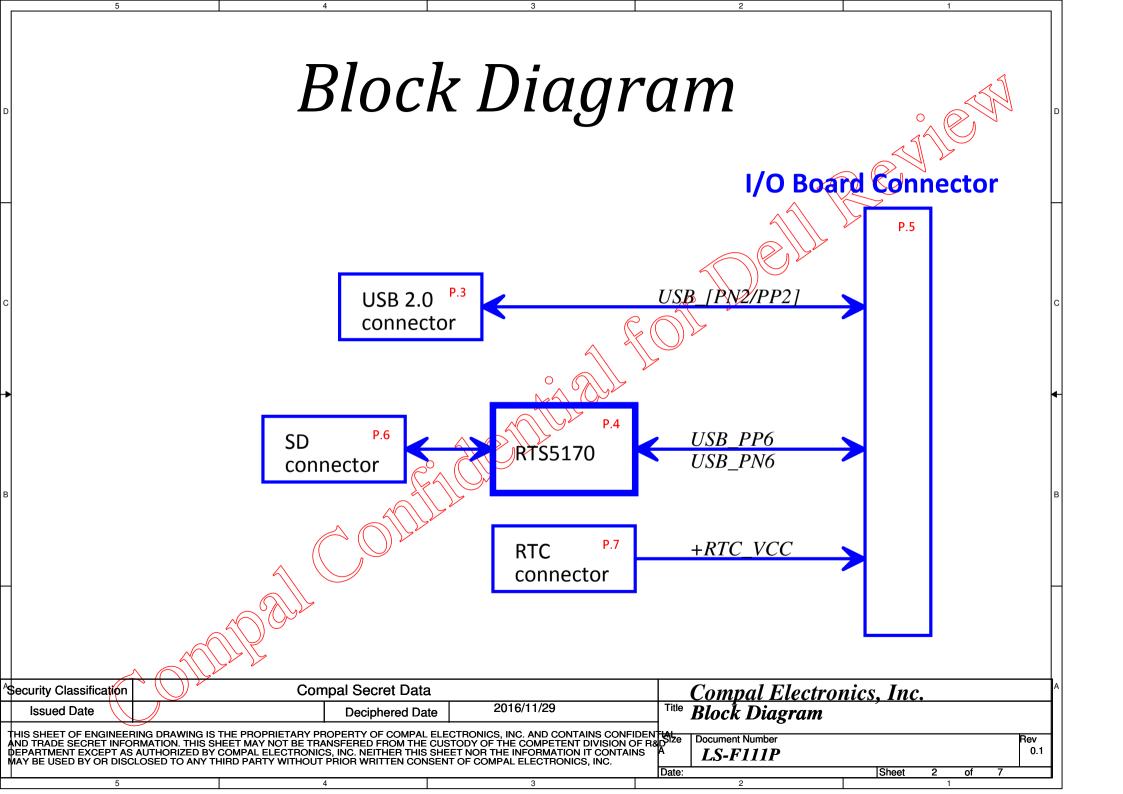
## Compal Confidential

## Schematic Document LS-F111P (IO/B)

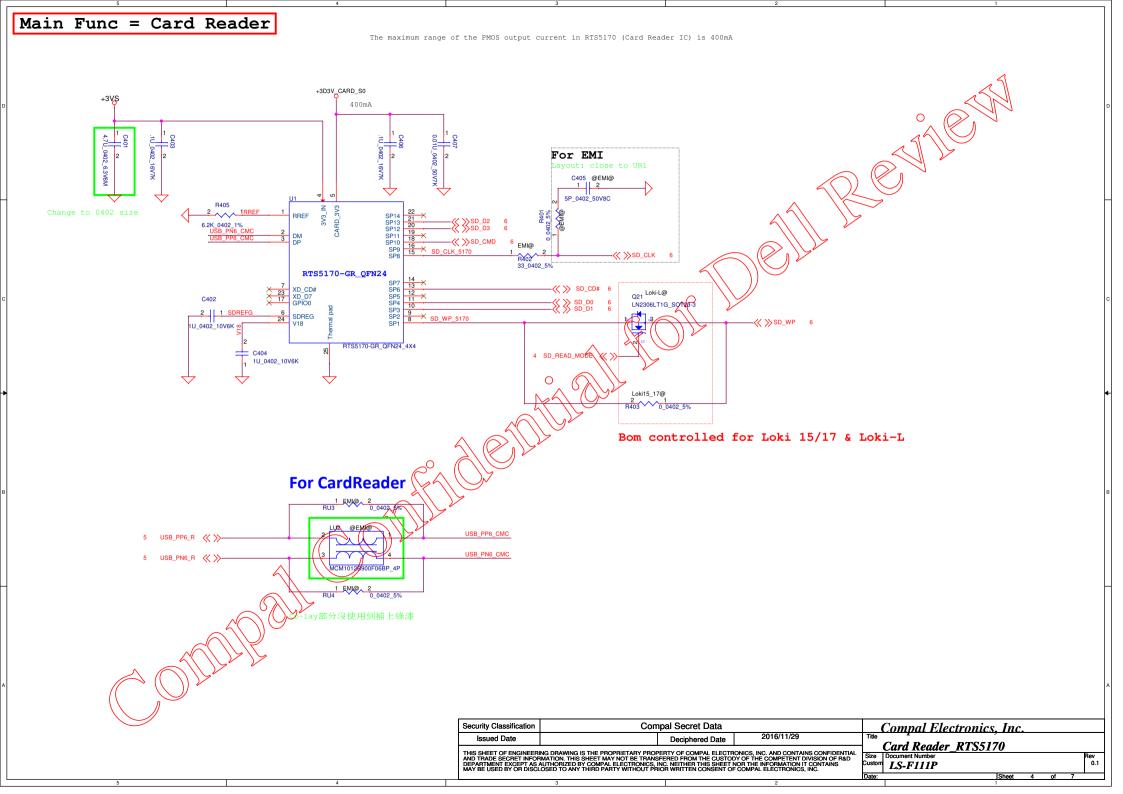
MODEL NAME : CAL50

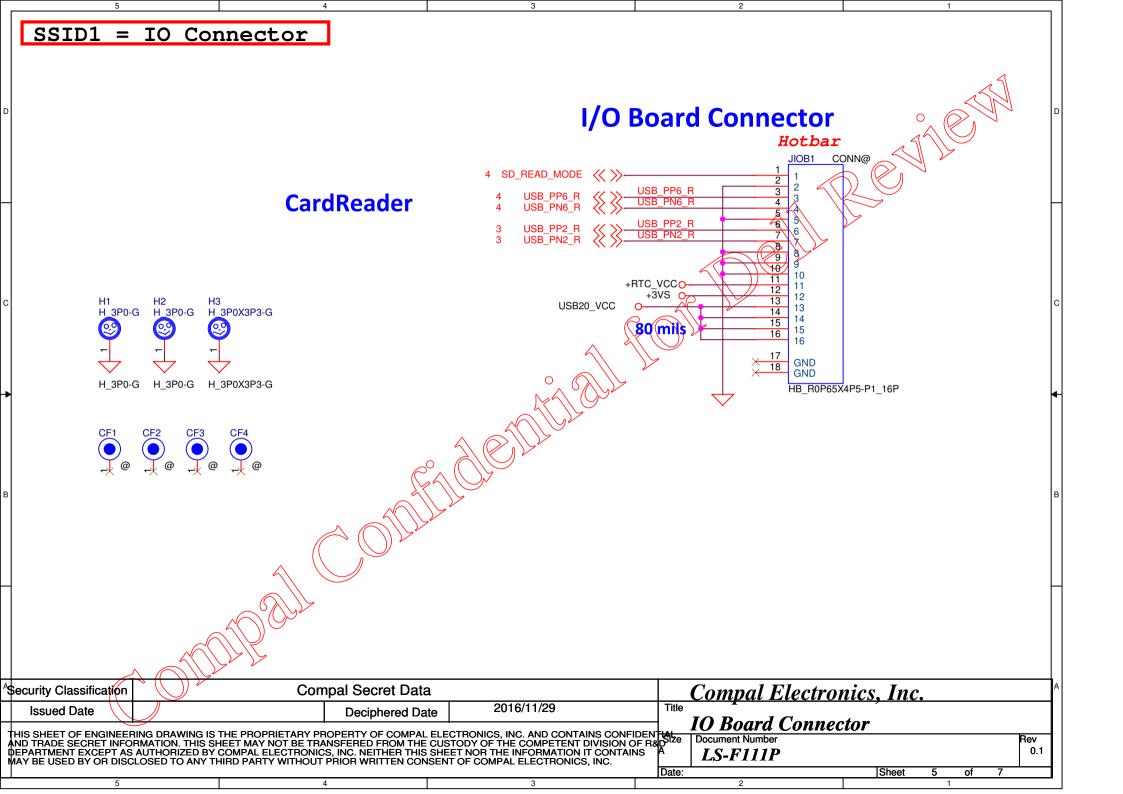
Date: 2016/11/29 Rev:1.0(A00)

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SSID1 = USB2.0 (Port 3)		222	JUSB3 CONN@
		DA6001TS011	
		PCB@	.1
		PCB 21C LS-F111P REV1 IO/B	DC233007P00 ACON_UARBG-4K1986
	<b>USB2.0</b>		
	000210		
From I/O Board Connector			
Trom 1/0 Board Connector	1 EMI@ 2 RU1 0_0402_5%		C2.45 (FOR 1 SP) CAL60
		1	JUSB3 CONN@
5 USB_PN2_R <b>≪ ≫</b>	LU1 @EMI@ 2 1	USB_PN2_CMC	
		USB_PP2_CMC	
5 USB_PP2_R <b>≪ ≫</b>	MCM1012B900F06BP_4P	038_FF2_CMC	
	1 EMI@ 2 RU2 0_0402_5%		ACON_UARF1-4K1926
	Co-lay部分沒使用到補上緣		$\sim \sim$ Change to DC021702200
	CO-Idy 动力 反使用到桶工廠		DI1 Use CH2.45 to layout L30ESDL5V0C3-2_SOT23-3 ESD@
		AF ( OS	
CH1 (FOR 2 SP)	CAL50		
	$\Delta P$		-
		2	$\bigtriangledown$
			USB20 VCC 20
	$ \land \bigcirc \lor \lor \lor$		
			100U_1206_6.3V6 22U_0805_10V6M 22U_0805_10V6M 22U_0805_10V6M 22U_0805_10V6K C302_10V6K C302_10V6K C301_10V6K
			2 2 2 2 2 2 2 2 2 2 2 2 2 2
			V6M V6K
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5		4	3			2		1
Main Func =	= Card F	Reader						
+3D3V_CARD_S0 -1U_0402_63V6M -1U_0400000000000000000000000000000000000	1 C410 2 10U_0805_10V6K	4 4 4 4 4 4 4	SD_D3       SD_CMD         SD_CLK (       >         SD_D0       >         SD_D1       >         SD_D2       >         SD_CD#       >         SD_WP       >	<u>1 2</u> R601 0 040	SD CD R#	D_S0 D0mA JSD1 1 2 DATA3 CMD 4 VSS 4 VSS 5 VSS2 DATA0 0 CLK 7 VSS2 DATA0 DATA1 0 DATA2 CHC T-SOL 5-531407 	CONN@ (RD) 12 (RD) 14 (RD) 14 (RD) 14 (RD) 14 (RD) 15 (RD)	°LOTA.
						¥		€ B
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	Issued Date		Deciphered Date	2016/11/29	)	Title		
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Main Func = 1	RTC			
D				
( <i>FO</i> R	<b>1 SP) CAL60</b> JRTC1 CONN@ 1 2 GND 4	+RTC_VC	(FOR 2 SP) CAL.	2c
	50271-0020N-001 00RO00	1 C701 0.1U_0402_10V7K	LOTES_AAA-BAT-054-K01 SP07000H700	•
В				В
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